US-PAT-NO:

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DOCUMENT-IDENTIFIER: US 4878166 A

TITLE:

Direct memory access apparatus and

methods for

transferring data between buses

having different

performance characteristics

----- KWIC -----

Detailed Description Text - DETX (242):

Because DMA transfers require little processor intervention, a large amount of data can be transferred concurrently with processor execution. For example, in multiprogramming or multitasking environments, other

processes may be

executed while a given process waits for a DMA channel to complete a data transfer.

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